3470

BOARD DIPLOMA EXAMINATION, (C–09)
APRIL/MAY—2015
DECE—FOURTH SEMESTER EXAMINATION

MICROPROCESSORS

Time : 3 hours]   [*Total Marks* : 80

PART—A   3×10=30

Instructions : (1) Answer **all** questions.

(2) Each question carries **three** marks.

(3) Answers should be brief and straight to the point and shall not exceed *five* simple sentences.

1. Explain stored program concept.

2. What is accumulator? State its importance in the CPU.

3. Explain sequential processing.

4. List the segment registers and write their uses.

5. Write need for interrupt.

6. Write the generalized instruction format of 8086.

7. What is a subroutine or a procedure?

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8. List any six branch instructions of 8086.

9. Write any three differences between RISC and CISC processors.

10. List any six features of 80386.

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PART—B   10×5=50

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Instructions : (1) Answer *any* **five** questions.

                (2) Each question carries **ten** marks.

                (3) Answers should be comprehensive and the criterion for valuation is the content but not the length of the answer.

11. *(a)* Explain cache memory organization.   5

     (b) Explain memory interleaving.   5

12. Explain zero address, one address, two address and three address instructions with one example each.

13. Explain about general purpose register, instruction register, instruction pointer, pointer and index registers.

14. Write the features of 8086 and draw the internal architecture of 8086 microprocessor.

15. Write an assembly language program to find the smallest of 'N' 8-bit number stored form SOURCE. Store the smallest number at RESULT.

16. Explain the assembly language development tools.

17. Write any five differences among 80286, 80386, 80486 and Pentium.

18. *(a)* Explain operating modes of 80286.   5

     (b) Explain memory management of 80286.   5

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