



C09-EC-404

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BOARD DIPLOMA EXAMINATION, (C-09)
OCT/NOV—2018
DECE—FOURTH SEMESTER EXAMINATION
MICROPROCESSORS

Time : 3 hours]

[Total Marks : 80

PART—A

3×10=30

- Instructions** : (1) Answer **all** questions.
(2) Each question carries **three** marks.
(3) Answers should be brief and straight to the point and shall not exceed *five* simple sentences.

1. Mention the stored program concept. 3
2. State the concept of memory interleaving. 3
3. State the need of bus control logic. 3
4. State the purpose of pointer and index registers. 1½+1½
5. State the importance of instruction pointer. 1½+1½
6. Draw the generalized instruction format of 8086. 3
7. Mention the uses of editor, assembler and linker. 1+1+1

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8. List any three ^{*} logic instructions of 8086. 3
9. State the features of 80486. 3
10. Mention the operating modes of 80286. 3

PART—B

10×5=50

Instructions : (1) Answer *any five* questions.

(2) Each question carries **ten** marks.

(3) The criterion for valuation is the content but not the length of the answer.

11. Explain zero address, one address, two address and three address instructions with examples. 2+3+3+2
12. Explain virtual memory organization. 10
13. Draw the pin diagram of 8086 and explain the significance of each pin. 3+7
14. Draw the internal architecture of 8086 and explain the significance and importance of functional units involved in the architecture. 5+5
15. Describe the procedure for executing assembly language program with an assembler. 10
16. Explain addressing modes of 8086 with examples. 6+4
17. Describe the architecture of 80386. 10
18. Explain the superscalar architecture. 10
