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BOARD DIPLOMA EXAMINATION, (C-14)  
MARCH/APRIL-2019  
DECE - FOURTH SEMESTER EXAMINATION  
LINEAR INTEGRATED CIRCUITS

Time: 3 Hours]

[Max. Marks: 80

## PART-A

3x10=30M

**Instructions:** 1) Answer **all** questions.  
2) Each question carries **three** marks.  
3) Answer should be brief and straight to the point and shall not exceed five simple sentences.

1. List the various levels of IC integration.
2. List any three advantages of SMT.
3. Define a) CMRR b) Slew rate.
4. Draw the circuit of inverting amplifier using OPAMP.
5. Compare voltage and current sweep generators and list any three applications.
- \* 6. Draw the circuit of Op Amp summing amplifier
7. Mention any three applications of Clippers.
8. Define a) Lock range b) capture range of PLL.
9. List any three advantages of instrumentation amplifier.
10. Define a) Resolution b) Settling time of DAC.

